



Quiz 1: Digital Logic Basis

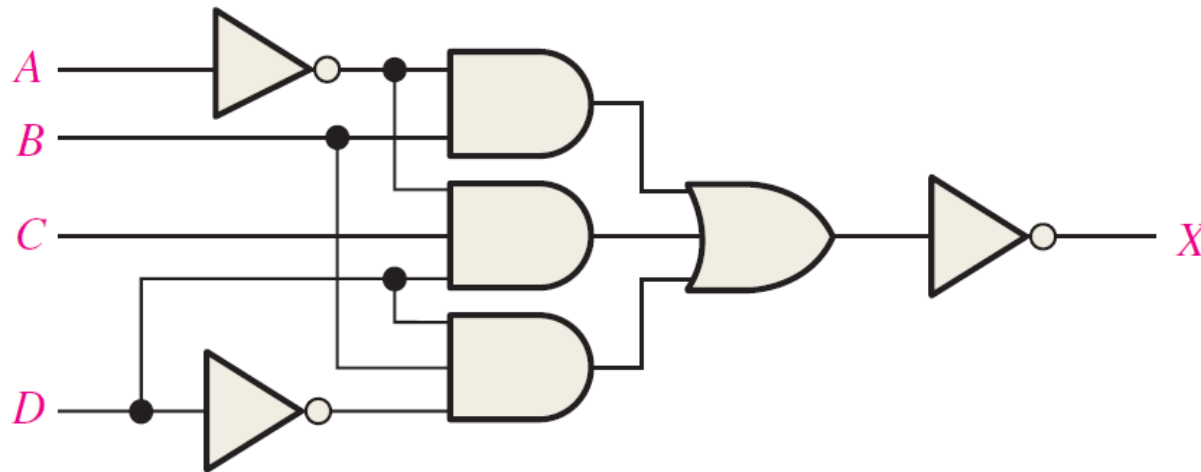


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Problems

1. Write the output expression for the circuit below



2. Use NAND gates, NOR gates, or combinations of both to implement the following logic expression

$$X = ABC\bar{D} + D\bar{E}F + \overline{AF}$$